## DEC 0 9 2003

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	09/234,233
Filing Date	anuary 20, 1999
nventor	Weimin Li, et al
Assignee Micron 1	Technology, Inc.
Group Art Unit	2818
Examiner	D. Vu
Attorney's Docket No	MI22-1035
Title: Semiconductor Processing Methods	

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## **INFORMATION DISCLOSURE STATEMENT**

References – See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed after the filing of the Request for Continued Examination (RCE) Application and prior to the receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: <u>09 Dec 2003</u>

James E. Lake

Reg. No. 44,854

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		LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Weimin Li et al					
DEC 0 9 2003			,				FILING DATE January 20, 1999			GROUP 2818		
Per mili	9				U.S. PATE	ENT DOCUMENTS						
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A	A	6,503,818 B1 01/03 Jang										
Al	В	6,486,	.061 B1	11/02	Xia et al.					v		
AC	С	6,486,	,057 B1	11/02	Yeh et al.						<u>.</u>	
AI	D	6,465	372 B1	10/02	Xia et al.							
Al	Е	6,444,	.593 B1	09/02	Ngo et al.							
Al	F	6,436,	.808 B1	08/02	Ngo et al.							
AC	G	6,435	943 B1	08/02	Chang et	al.					,	
Ai	н	6,429	115 B1	08/02	Tsai et al.				,			
А	VI	6,284	.677 B1	09/01	Hsiao et a			X.				
A	J	6,030,901		02/00	Hopper et al.						,	
Al	к	6,133,618		10/00	Steiner							
A	L	5,994,217		11/99	Ng							
AM	vi.	5,985	,519	11/99	Kakamu e							
,				1	FOREIGN PA	ATENT DOCUMEN	TS					
Document Number			Date		Country		Class	Sul	oclass	Trans		
Aì	.,	80461	0.6	02/96	Japan	<del></del>					Yes	No
AG	T	80461		02/96	Japan							
A		72017		08/95	Japan					`		
AG	Q	62321	, ,	08/94	Japan							
Al	R											
			ОТН	IER REFERENC	CES (includin	g Author, Title, Date	e, Pertinent Pages, Etc.)	)				
AS		Wolf, S., "Silicon Processing for the VLSI Era," Vol. 1, pps. 189-190.										
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EXAMINER						DATE CONSIDER	RED					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.												

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DEC 0 9 2003					APPLICANT Weimin Li et al							
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*Examiner Initial		Docu Numl	ment ber	Date	Nan	ne	•	Class	Subclass	ass Filing Date If Appropriate		
	AA	5,747	,388	05/98	Küsters +							
	AB	5,639	,687	06/97	Roman +							
	AC	2001/	/0003064	6/01	Ohto		<u>-</u> .					
	AD	2003/	/0013311 	1/03	Chang et al.	Chang et al.						
	AE											
	AF											
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	AR		Wolf, S., "Silicon	Processing for	the VLSI Era," Vol. 1, pps. 40	07-41	3.			_		
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EXAMINER DATE CONSIDERED												
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.												

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Under the Paperwork Reduction Act of 1995, no		re required to respond to a collection Application Number	of info 09/234	rmation unless it displays a valid OMB control number. 4,233			
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FORM	T	First Named Inventor	Weimi	in Li			
(to be used for all correspondence after initial filin	ng)	Art Unit	2818				
	П	Examiner Name	D. Vu				
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or Reg. No. 44,854, Wells St. J	lohn P.S.						
Individual Signature							
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.